

ET74HCT86 - Quadruple 2-Input XOR Gates

General Description

This device contains four independent 2-input XOR gates. Each gate performs the Boolean function $Y = A \oplus B$ in positive logic.

Features

- Buffered Inputs: TTL Level
- Wide Operating Voltage Range: 4.5 V to 5.5 V
- Wide Operating Temperature Range: -40°C to +125°C
- Supports Fan-Out Up to 10 LSTTL Loads
- Significant Power Reduction Compared to LSTTL Logic ICs
- ESD Protection:
 - HBM JESD22-A114-A Exceeds 2000 V
 - CDM JESD22-C101-A Exceeds 1000 V
- Latch-up Performance Exceeds 200 mA

Applications

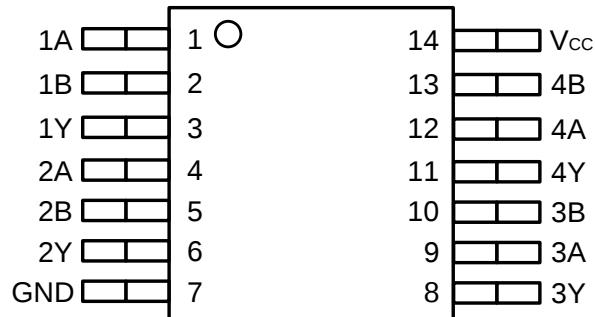
- Detect Phase Differences In Input Signals
- Create a Select-able Inverter / Buffer

Device Information

Part No.	Package	Packing Option	MSL
ET74HCT86M	SOP14	Tape and Reel, 4K	3
ET74HCT86V	TSSOP14	Tape and Reel, 4K	3

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Pin Configuration



Top View

Pin Functions

Pin		I/O	Description
Name	No.		
1A	1	Input	Channel 1,Input A
1B	2	Input	Channel 1,Input B
1Y	3	Output	Channel 1,Output Y
2A	4	Input	Channel 2,Input A
2B	5	Input	Channel 2,Input B
2Y	6	Output	Channel 2,Output Y
GND	7	—	Ground
3Y	8	Output	Channel 3,Output Y
3A	9	Input	Channel 3,Input A
3B	10	Input	Channel 3,Input B
4Y	11	Output	Channel 4,Output Y
4A	12	Input	Channel 4,Input A
4B	13	Input	Channel 4,Input B
V _{CC}	14	—	Positive Supply

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Block Diagram

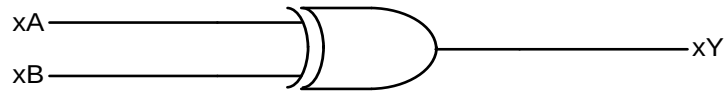


Fig1. Logic Symbol

Functional Description

Function Table

Input		Output
xA	xB	xY
L	L	L
L	H	H
H	L	H
H	H	L

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter		Value	Unit
V _{CC}	Supply Voltage		-0.5 to 7.0	V
I _{IK}	Input Clamp Current ⁽¹⁾	V _I < -0.5V or V _I > V _{CC} + 0.5V	±20	mA
I _{OK}	Output Clamp Current ⁽¹⁾	V _O < -0.5V or V _O > V _{CC} + 0.5V	±20	mA
I _O	Continuous Output Current	V _O > -0.5V or V _O < V _{CC} + 0.5V	±25	mA
I _{CC}	Continuous Current through V _{CC} or GND		±50	mA
T _J	Max Junction Temperature		150	°C
T _{LEAD}	Lead Temperature (Soldering 10s)		300	°C
T _{STG}	Storage Temperature		-65 to 150	°C
V _{ESD}	ESD Human Body Model (JESD22-A114)		±2000	V
	ESD Charged Device Model (JESD22-C101)		±1000	
I _{LU}	Max Latch up Current (EIA/JESD78E)		±200	mA

Note1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

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Thermal Characteristics

Symbol	Thermal Metric	Package		Unit
		SOP14	TSSOP14	
R _{θJA}	Junction-to-ambient thermal resistance	120	150	°C/W

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter		Min	Max	Unit
V _{CC}	Supply Voltage		4.5	5.5	V
V _{IH}	High-level Input Voltage	V _{CC} = 4.5 V to 5.5 V	2		V
V _{IL}	Low-level Input Voltage	V _{CC} = 4.5 V to 5.5 V		0.8	V
V _I	Input Voltage		0	V _{CC}	V
V _O	Output Voltage		0	V _{CC}	V
T _A	Operating Free Air Temperature		-40	125	°C
t _t	Input Transition Time	V _{CC} = 4.5 V		500	ns
		V _{CC} = 5.5 V		400	

Operating Characteristics

Over operating free-air temperature range; typical values measured at T_A = 25°C (unless otherwise noted)

Symbol	Parameter	Condition	V _{CC}	Min	Typ	Max	Unit
C _{PD}	Power Dissipation Capacitance Gate	No Load	5V		12		pF

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Electrical Characteristics

Over operating free-air temperature range; typical values measured at $T_A = 25^\circ\text{C}$ (unless otherwise noted).

Symbol	Parameter	Condition		V _{CC}	Operating Free-air Temperature (T _A)								Unit
					T _A =25°C			-40°C≤T _A ≤85°C		-40°C≤T _A ≤125°C			
					Min	Typ	Max	Min	Max	Min	Max		
V _{OH}	High-Level Output Voltage	V _I = V _{IH} or V _{IL}	I _{OH} =-20μA	4.5 V	4.4			4.4		4.4		V	
			I _{OH} =-4mA	4.5 V	3.98			3.84		3.7			
V _{OL}	Low-Level Output Voltage	V _I = V _{IH} or V _{IL}	I _{OH} =20μA	4.5 V			0.1		0.1		0.1	V	
			I _{OH} =4mA	4.5 V			0.26		0.33		0.4		
I _I	Input Leakage Current	V _I = V _{CC} and GND	I _O = 0	5.5 V			±0.1		±1		±1	μA	
I _{CC}	Supply Current	V _I = V _{CC} or GND	I _O = 0	5.5 V			2		20		40	μA	
ΔI _{CC} ⁽²⁾	Additional Quiescent Device Current Per Input Pin.	V _I = V _{CC} - 2.1		4.5 V to 5.5 V		100	360		450		490	μA	
C _I	Input Capacitance			5 V			10		10		10	pF	

Note2: For dual-supply systems theoretical worst case ($V_I = 2.4 \text{ V}$, $V_{CC} = 5.5 \text{ V}$) specification is 1.8 mA.

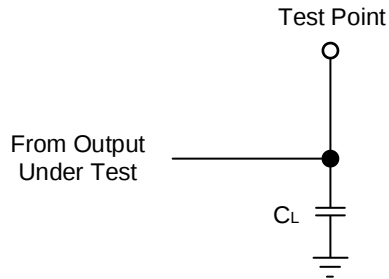
Switching Characteristics

Over operating free-air temperature range; typical values measured at $T_A = 25^\circ\text{C}$ (unless otherwise noted)

Symbol	Parameter	From	To	Condition	V _{CC}	Operating Free-air Temperature (T _A)						Unit	
						T _A =25°C			-40°C≤T _A ≤85°C		-40°C≤T _A ≤125°C		
						Min	Typ	Max	Min	Max	Min		Max
t _{pd}	Propagati on Delay	A or B	Y	C _L = 50 pF	4.5 V to 5.5V		4	7		8		9	ns
		A or B	Y	C _L = 15 pF	4.5 V to 5.5V		3	5		8		9	
t _t	Transition -time		Y	C _L = 50 pF	4.5 V			7		8		10	ns

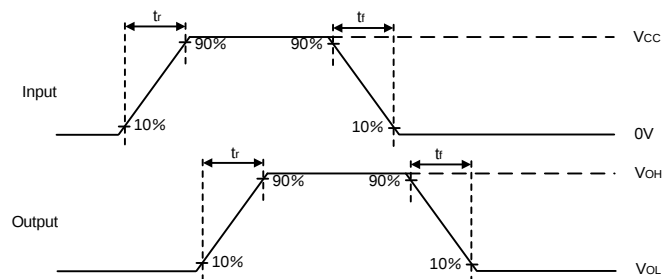
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Parameter Measurement Information



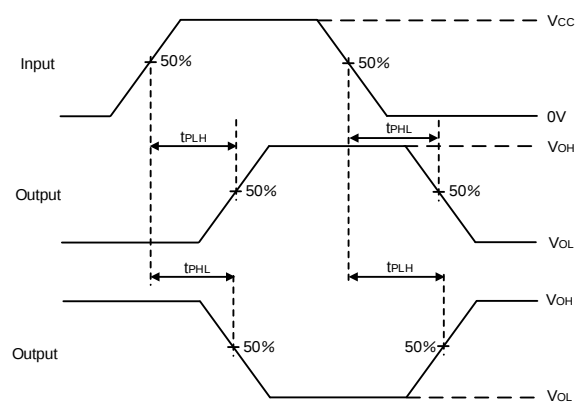
$C_L = 50 \text{ pF}$ and includes probe and jig capacitance

Fig2. Load Circuit



t_t is the greater of t_r and t_f

Fig3. Voltage Wave-forms Transition Times



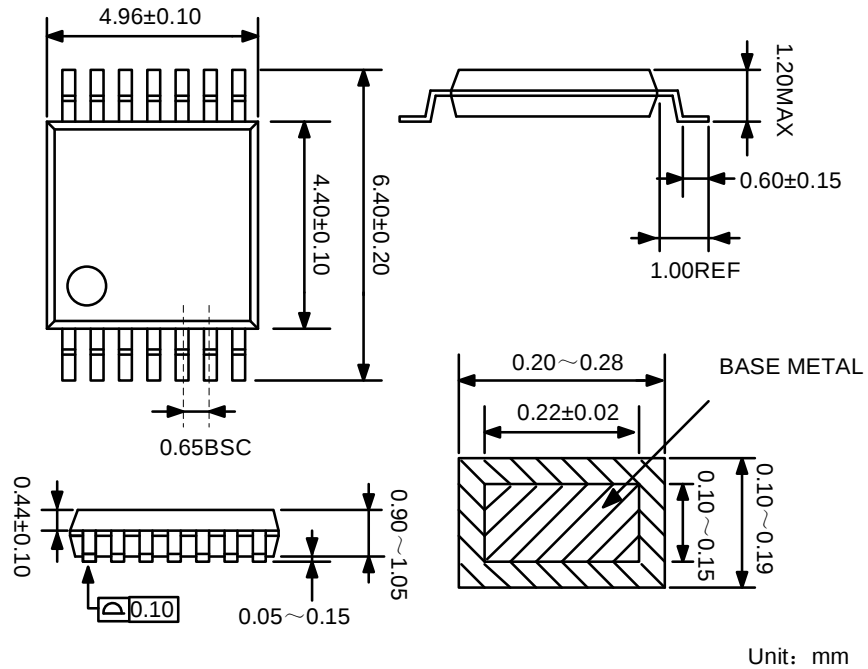
The maximum between t_{PLH} and t_{PHL} is used for t_{pd}

Fig4. Voltage Wave-forms Propagation Delays

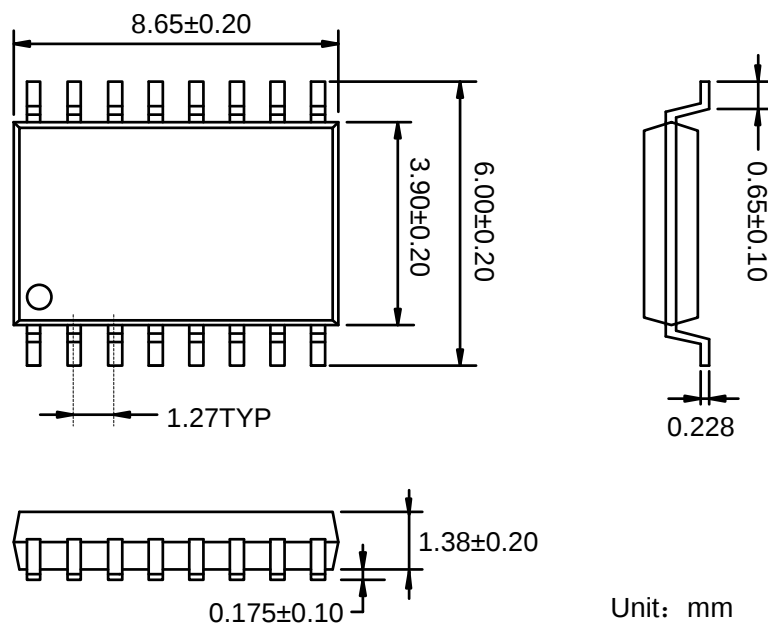
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Package Dimension

TSSOP14



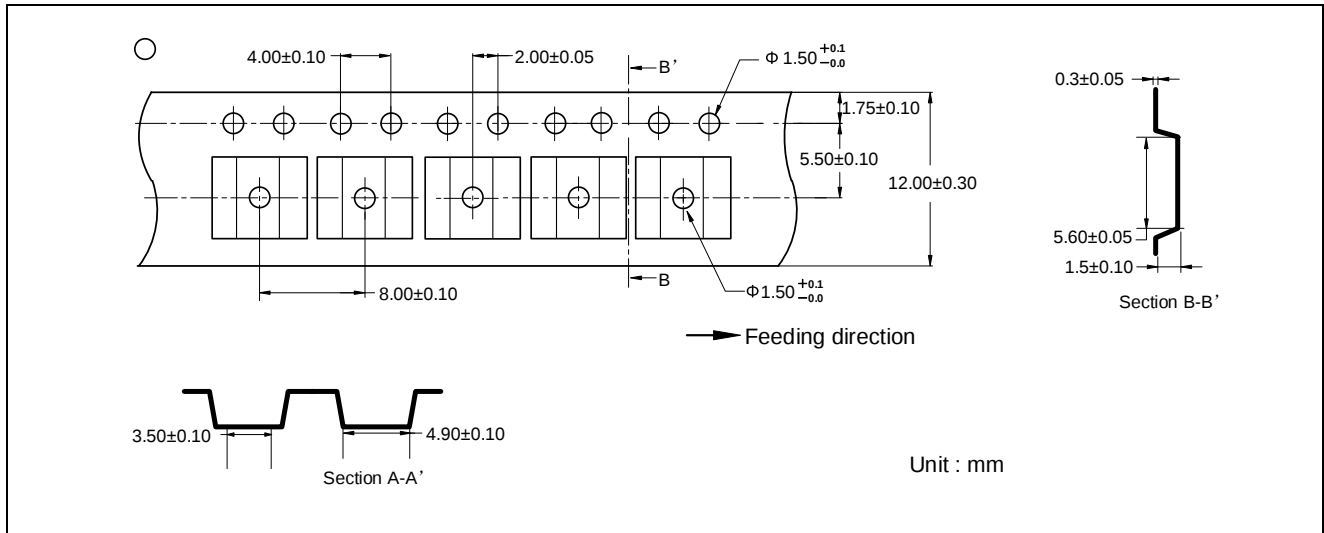
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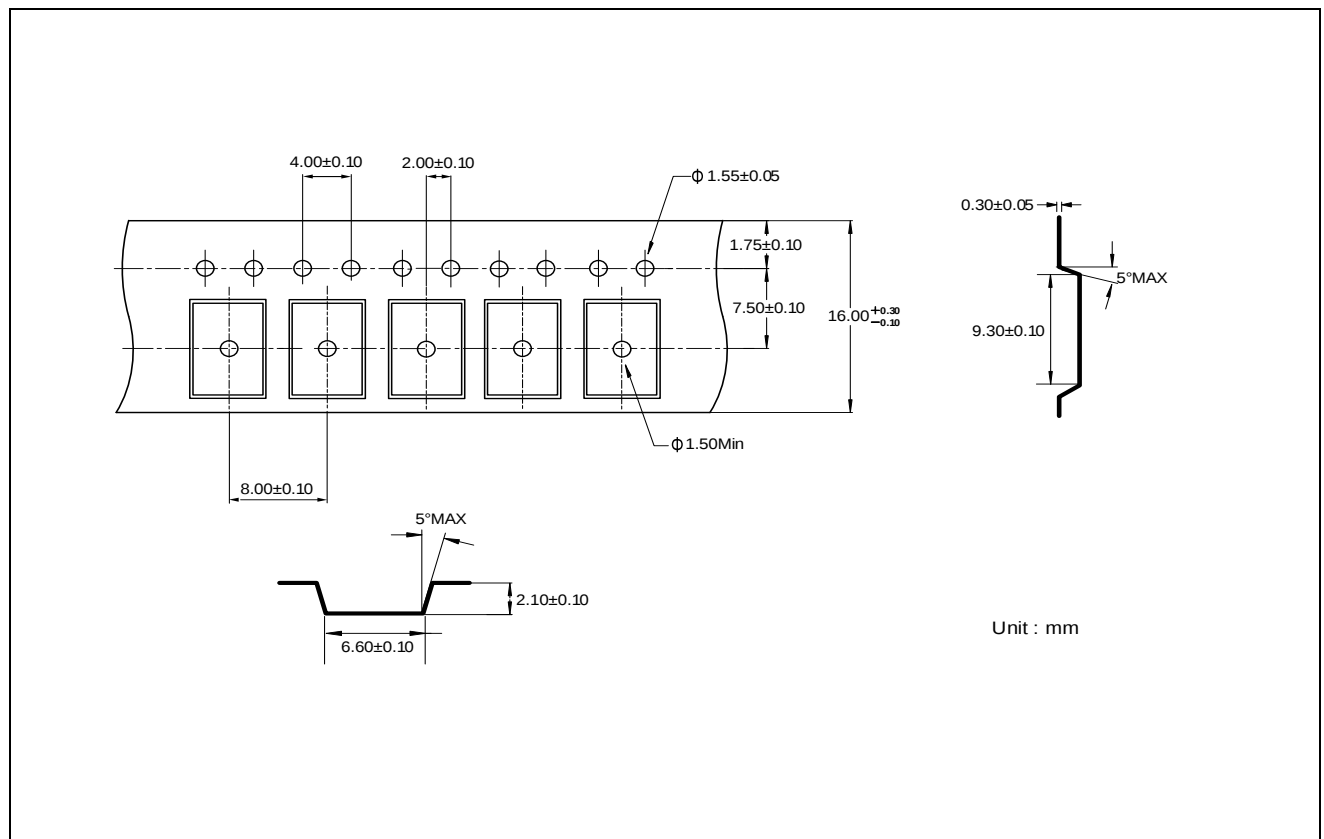
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Tape Information

TSSOP14



SOP14



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Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2023-08-10	Preliminary Version	Wangar	Tugz	Liujiy
1.0	2025-6-19	Official Version	Yuyf	Tugz	Liujiy